

Refine Search

10/673,539

Search Results -

Terms	Documents
L6 and (isolated or isolating) and (lateral or laterally)	0

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L7

Search History

DATE: Friday, October 15, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=USPT; PLUR=YES; OP=ADJ

Hit Count Set Name

result set

<u>L7</u>	L6 and (isolated or isolating) and (lateral or laterally)	0	<u>L7</u>
<u>L6</u>	L5 and shielding	9	<u>L6</u>
<u>L5</u>	L4 and (conductive adj layer)	62	<u>L5</u>
<u>L4</u>	L3 and (n adj type) and (p adj type)	143	<u>L4</u>
<u>L3</u>	L2 and ((second) near2 (source or drain))	182	<u>L3</u>
<u>L2</u>	L1 and (conductivity adj type)	654	<u>L2</u>
<u>L1</u>	mos near10 silicide	2333	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

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<u>L2</u>	L1 and (conductivity adj type)	654	<u>L2</u>
<u>L1</u>	mos near10 silicide	2333	<u>L1</u>

END OF SEARCH HISTORY

Hit List

Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs
Generate OACS				

Search Results - Record(s) 1 through 9 of 9 returned.

☐ 1. Document ID: US RE38296 E

L6: Entry 1 of 9

File: USPT

Nov 4, 2003

US-PAT-NO: RE38296

DOCUMENT-IDENTIFIER: US RE38296 E

TITLE: Semiconductor memory device with recessed array region

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw D
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☐ 2. Document ID: US 6600160 B2

L6: Entry 2 of 9

File: USPT

Jul 29, 2003

US-PAT-NO: 6600160

DOCUMENT-IDENTIFIER: US 6600160 B2

**** See image for Certificate of Correction ****

TITLE: Photoelectric converter and radiation reader

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw D
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☐ 3. Document ID: US 5581106 A

L6: Entry 3 of 9

File: USPT

Dec 3, 1996

US-PAT-NO: 5581106

DOCUMENT-IDENTIFIER: US 5581106 A

TITLE: Semiconductor memory cell having information storage transistor and switching transistor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw D
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☐ 4. Document ID: US 5578853 A

L6: Entry 4 of 9

File: USPT

Nov 26, 1996

US-PAT-NO: 5578853

DOCUMENT-IDENTIFIER: US 5578853 A

TITLE: Semiconductor memory cell having information storage transistor and switching transistor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. De
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☐ 5. Document ID: US 5578852 A

L6: Entry 5 of 9

File: USPT

Nov 26, 1996

US-PAT-NO: 5578852

DOCUMENT-IDENTIFIER: US 5578852 A

TITLE: Semiconductor memory cell having information storage transistor and switching transistor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. De
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☐ 6. Document ID: US 5576571 A

L6: Entry 6 of 9

File: USPT

Nov 19, 1996

US-PAT-NO: 5576571

DOCUMENT-IDENTIFIER: US 5576571 A

TITLE: Semiconductor memory cell having information storage transistor and switching transistor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. De
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☐ 7. Document ID: US 5506436 A

L6: Entry 7 of 9

File: USPT

Apr 9, 1996

US-PAT-NO: 5506436

DOCUMENT-IDENTIFIER: US 5506436 A

TITLE: Semiconductor memory cell

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. De
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☐ 8. Document ID: US 5428238 A

L6: Entry 8 of 9

File: USPT

Jun 27, 1995

US-PAT-NO: 5428238

DOCUMENT-IDENTIFIER: US 5428238 A

TITLE: Semiconductor memory cell having information storage transistor and switching transistor

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWC	Draw De
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☐ 9. Document ID: US 5196910 A

L6: Entry 9 of 9

File: USPT

Mar 23, 1993

US-PAT-NO: 5196910

DOCUMENT-IDENTIFIER: US 5196910 A

TITLE: Semiconductor memory device with recessed array region

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWC	Draw De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Terms	Documents
L5 and shielding	9

Display Format:

[Previous Page](#)

[Next Page](#)

[Go to Doc#](#)